Dr. Ajay Dadasaheb Jadhav

M.E. (Electronics), Ph.D. (Electronics Engineering)

LMISTE, LMISI

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Warje, Pune- 411052.

CURRICULUM VITAE

Academic Performance:

- Ph.D. Electronics Engineering, Analog to Digital Converters for Software Defined Radios, Shivaji University Kolhapur, Maharashtra, December- 2009
- M.E.- Electronics Engineering, Shivaji University, Kolhapur, First class with Distinction [2000]
- Diploma- Computer Science, Shivaji University, Kolhapur, First class with Distinction [1990]
- B.E.- Electronics Engineering, Shivaji University, Kolhapur, First class with Distinction [1987]

Work Experience: 38 years

Teaching: 36.5 Years

- Principal, Dnyanshree Institute of Engineering & Technology, Sajjangad Road, Satara. Duration – July 30, 2013 to till date- Approved
- Professor, Sinhgad College of Engineering, Pune. Duration- December 28, 2009 to June 29, 2013.- Approved
- Assistant Professor, Sinhgad College of Engineering, Pune. Duration- November 20, 2001 to December 12, 2009.- Approved
- Assistant Professor, Pune Institute of Computer Technology, Pune. Duration- January 09, 2001 to November 19, 2001- Regular
- Lecturer/Sr. Lecturer (Scale), K.B.P. College of Engineering & Polytechnic, Satara. Duration- March 20, 1989 to January 27, 2001 – Permanent

Professional: 1.5 Years

- R & D Engineer, Anshuman, Pune Duration- February 01, 1988 to March 15, 1989

- Trainee R & D Engineer, Micromax, Pune. Duration- August 01, 1987 to January 31, 1988.

Major Responsibilities Handled:

1. Former Member, Academic Council, Dr. Babasaheb Ambedkar Technological university, Lonere.
2. Co-coordinator of DBATU Regional Center & Sub Center
3. Member of DBATU Equivalence Committee
4. Member of DBATU Planning and Evaluation (Monitoring) Board
5. Member of DBATU Examination Board
6. Member, Academic Council, Sharad Institute of Technology, COE Yadrav, Ichalkarnji
7. Member, Board of Studies, KIT College of Engineering (E&TC) Engineering
8. Ph.D. Guide recognition of Dr. Babasaheb Ambedkar Technological university, Lonere.
9. Ph.D. Guide recognition of Shivaji University, Kolhapur, guided 4 students
10. Former Ph.D. Guide recognition of University of Pune
11. Postgraduate Teacher recognition of Pune University
12. Postgraduate Teacher recognition of Shivaji University, Kolhapur
13. Member Coordination Committee [Adhoc Board: Electronics & Electrical Engineering in Shivaji University, Kolhapur]
14. Member BoS [Adhoc Board: Electronics & Telecommunication Engineering in Shivaji University, Kolhapur]
15. Member, Rotary Club, Satara.
16. Selection Committee Member at various Universities
17. LIC Committee Chairman & Member in various Universities
18. Expert for Ph.D. Synopsis Scrutiny Committee, Shivaji University, Kolhapur
19. U.G. / P.G. Syllabus setting of various universities
20. Paper setting in various universities
21. Examiner for various examinations
22. NBA activity planning & implementation in the department
23. Nodal officer for Finance for TEQIP
24. Conduction of STES central ME entrance examination at the Department
25. Boosting T&P activity for PG students
26. Organized various Industrial Training Programs for Faculty, staff and Students
27. P.G. Coordinator for M.E. (Digital Systems- Electronics)
28. Senior Supervisor
29. Best Teacher Honor at Sinhgad College of Engineering
30. NBA report Preparation of UG E &Tc Department

Responsibilities Handled as a Principal:

1. Appointed as a Principal in Dnyanshree Institute of Engineering & Technology, Sajjangad Road, Satara (DIET) in June 2013
2. Developed newly established institute
3. Establishes first incubation centre in the Satara District at the DIET campus
4. Developed various departments and laboratories
5. Establishes academic processes
6. Developed & introduce new Administrative Processes
7. The institute has achieved AICTE Best Upcoming Technical Institute Award under the leadership in 2016
8. Developed Workshop in Institute
9. Reforms in Academic & Administrative formats
10. Establishment of professional bodies in the institute
11. Collaboration with best industries
12. Establishes Centre of Excellence in DIET Campus with collaboration of Mitsubishi
13. Institute has achieved NAAC Accreditation with B+ Grade in 2023

Research Experience:

1. Ph.D. Thesis: Studies on an Analog to Digital Converter for Software Defined Radio
2. Research Experience: 22 years
3. Designed and developed various industry projects
4. Designed and Developed Microprocessor/ Microcontroller trainers, interfaces, systems for various applications
5. Organized Industrial Training Programs for staff and students
6. Industrial Consultancy for providing solutions to industrial problems in system design and development
7. 20 Postgraduate students guided
8. Publications (around 50+) in referred national/International Journals/ Conferences
9. Worked as reviewer for various Journals/Conferences
10. Worked as resource person/Key note speaker for various conferences.
11. Sponsored Projects from Industries
12. Resource and organization of hands on industrial training program for students on various design tools and techniques to inculcate research attitude

13. **Best Paper Award** at National Conference, TSyGECA, January 23-24, 2006, @ Government College of Engineering, Aurangabad.

Organization of Technical Activities/Programs:

1. Co-coordinator of two-day workshop on 'Linux'
2. Coordinator of state level three-day industry sponsored workshop on 'Microcontroller Design & Development Tools'
3. Co-convener of 'Innovation 2008' National conference
4. Coordinator of ' ePGCON 2009' PG student conference
5. Convener of industry sponsored national level Two Week STTP on 'Embedded System Design' 2010
6. Hands on industrial training program for post graduate students on tools and techniques to inculcate research attitude
7. Convener National Conference @ Department of Electronics & Telecommunication, Sinhgad College of Engineering, Pune
8. Convener TEQIP Sponsored Staff Development Program@ Department of Electronics & Telecommunication, Sinhgad College of Engineering, Pune
9. National level three-day industry sponsored workshop on '**Verilog -- Stepping Stone for SoC Verification'** Sponsored by Teckschool Pune & "Mentor Graphics" Bangalore

Study Material Preparation:

Major contribution **in MSBTE Projects on Laboratory Manuals for 03 subjects and MSBTE Projects on Question Bank for 02 subjects**

Funding/ Grants/Other Information:

- **AICTE MODROBS** on 'Embedded System Design Laboratory' Rs. 10,00,000/- for Electronics & Telecommunication Department
- Industry sponsored national & state level workshop/ training program
- SpoC Infosys Campus Connect Program at SCOE: Conduction of Infosys foundation program

Short Term Courses / Seminars/Workshops (Conducted)

1. **Convener:** Two-week National Level Short Term Training Program on "Embedded System Design" @Sinhgad College of Engineering, Pune, May 10-22, 2010 in association with SPJ Embedded Technologies, Logic Power, Tech LABS, BUREAU VERITAS, ORCAD
2. **Convener:** Two-day workshop on "Embedded System Design" For M.E. students in association with SPJ Embedded Technologies, Pune. March 21-22, 2010
3. **Co-Coordinator:** Conference "ePGCON 2009" for M.E. Students @ Sinhgad College of Engineering, Pune, April 25, 2009
4. **Co-Convener:** National conference "Innovation 2008" for Engineering Teachers @ Sinhgad College of Engineering, Pune. November 17-18, 2008
5. **Coordinator:** Three-day Industry Sponsored Workshop on "Microcontroller Design and Development Tools" @ Sinhgad College of Engineering, Pune. July 7-9, 2005 in association with Prism Technologies, SPJ Systems, ADM
6. **Co-Coordinator:** Three-day Workshop on "Windows/Linux Server Administration" @ Smt. Kashibai Navale College of Engineering, Pune.July 2-4, 2005

Invited Lectures / Resource Person / Short Term Courses / Seminars/Workshops (Attended)

1. AICTE-ISTE STTP on Microcontrollers in Power & Industrial Control @ K.B.P. College of Engineering and Polytechnic, Satara Resource Person: Topic: 8051 and 8096 Microcontroller Architecture and System Design
2. AICTE-ISTE STTP on DSP Design & Development Tools (MATLAB and Code Design Studio) @ Pune Institute of Computer Technology, Pune Resource Person:Topic: C Programming using Code Composer Studio / MATLAB Programming
3. AICTE-ISTE STTP on Embedded System Design @ Dr. Babasaheb Ambedkar Technological University, Lonere Resource Person: Topic: DSP Processor Architecture and Interfacing (Texas Instruments and Analog Devices)
4. AICTE-ISTE STTP on Software Defined Radios @ J.S.P.M's Shahu College of Engineering, Pune Resource Person: Topic: RF implementation issues in Software Defined Radios and Analog to Digital Converters for Software Defined Radios
5. Invited Lecture on 'Designing with 8051 Microcontroller' in national level training program for industry, arranged by SPJ Systems, Pune
6. Resource Person on "Designing with PIC Microcontroller" in teachers training for polytechnic at Smt. Bhivarabai Sawant Polytechnic, Wagholi, Pune
7. Invited Lecture on 'Embedded System Design_ A Hardware Approach' in VLC program of MSBTE- 2010

8. Invited Lecture (Guest Speaker) on 'Hardware Aspects of Embedded System Design Using ARM Processor' in International Conference and Workshop on Emerging Technology (ICWET), February 2010@ Thakur College of Engineering & Technology, Kandavali, Mumbai 2010

9. Invited Lecture on 'Embedded System Design and ARM Processor' in Department of Instrumentation, AISSMS IOIT, Pune 2010

10. Session Judge at National Conference NCPC at Sinhgad College of Engineering Pune in 9-10, April 2010

11. Judge for evaluation of BE projects, at SKN COE, Pune

12. Guest Lecture on Embedded Systems, Malegaon

13. Session Judge at International Conference and Workshop on Emerging Technology (ICWET) at Thakur College of Engineering & Technology, Kandavali, Mumbai in February 2010

14. Resource Person on "System on Chip" in teachers training at MARATHAWADA MITRA MANDAL'S COLLEGE OF ENGINEERING, Kothrud, Pune in 2011

15. Resource Person on "Testing & Troubleshooting" in Staff Development Program at Sinhgad College of Engineering Pune in 2012

16. Resource Person on "Sequential circuit design" in Faculty Training Program arranged by BoS Electronics & Telecommunication Engineering, University of Pune at Sinhgad College of Engineering Pune in 2013

17. Chief Guest & Resource person for Tech++2014 @ Yashvantrao Chavan Institute of Science, Satara in 2014.

18. Plenary Talk at National Conference on "Modern Approach for Green Electronics & Computing 2014" @ Yashvantrao Chavan Institute of Science, Satara in 2014.

19. Resource Person for Workshop on Embedded Systems @ Trinity College of Engineering, Pune

20. 38th International Conference on VLSI Design 24th International Conference on Embedded Systems 2025.

Technical Attainment:

1. Patent (Granted): 15 February 2024
2. Patent (Published): Mix Logic Design

PUBLICATIONS:

Research Papers Published: (National/ International Conferences/ Journals)

2003-2004

1. Ajay Jadhav, Umashankar More, "The Seamless Radio- Software Radio", National Conference, November 2003@ IET Bhaddal, Minapur-Punjab, Proceeding.

2004-2005

1. Deshpande Swanand, Ajay Jadhav, "Vision for Robot Positioning: A Neural Network Based Approach", International Conference on Instrumentation, Proceeding, INCON-2004 December 2004 @ PIET Government College of Engineering, Pune.

2005-2006

1. Jadhav A.D., Yardi A.R., Dr. Bodhe S.K., "Performance Evaluation Model for ADC Characterization", National Conference, Proceeding, TSyGECA, January 23-24, 2006, @ Government College of Engineering, Aurangabad.

2006-2007

1. Jadhav A.D., Yardi A.R., Dr. Bodhe S.K., "Evaluation of Specifications of ADC for Software Defined GSM Radio ", International Conference on Recent Trends in Automation & its Adaption to Industries, Proceeding, July 11-13, 2006, @ Priyadarshani College of Engineering & Architecture, Nagpur.
2. Neha Ganvir, Ajay Jadhav, "ARM Based JPEG Encoder", National Conference, Proceeding pp., Technobuzz 2007, February 16-17, 2007, @ Venutai Chavan Polytechnic, Pune.

2007-2008

1. Aparna Dhavlikar , Ajay Jadhav, A. R. Yardi "Performance Analysis for 10 bit Pipeline ADCs", National Conference, Proceeding pp., April 28,2008,@ MIT College of Engineering, Alandi, Pune.
2. Kamat D.K., A.D.Jadhav, "Performance Comparison of Various Hardware Implementations of AES algorithm", National Conference, Proceeding, ORION 08, March 29-30,2008,@ Pravara Rural Engineering College, Loni.
3. Kamat D.K., A.D.Jadhav, "Hardware Implementation of AES algorithm- Handel-C approach", National Conference, Proceeding pp., ETEM 08, April 28,2008,@ Rajarshi Shahu College of Engineering, Pune.
4. Jadhav A.D., Yardi A.R., Dr. Bodhe S.K., Jagtap J.B., "Modeling Specifications of Flash ADC for RF Design in Software Defined GSM Mobile Station Radio Receiver", International Conference on Sensors, Signal Processing, Communication, Control & Instrumentation, Proceeding pp., SSPCCIN, January 3-5,2008, @ VIT College of Engineering, Pune.

5. A.D. Jadhav, A.R. Yardi, Dr. S.K. Bodhe," Characterization of ADC for Software Defined GSM Radio Receiver," International Journal of Engineering & Industrial Applications (IJERIA), ISSN 0974-1518, Vol. No. 1, No. II (2008), pp 105-124. (ISSN: 0974-1518)

2008-2009

1. Kamat D.K., A.D.Jadhav, "Design and Implementation of a Semiconductor ASIC for Security Using AES algorithm", Pravara International Conference on Emerging Trends in Engineering (PICETE-2008), Proceeding pp., December 20-22, 2008, @ Pravara Rural Engineering College, Loni.

2. Jyoti Lele, Ajay Jadhav, "Neural Network Based Speaker Recognition System", 2nd National Conference Mathematical Techniques Emerging Paradigms for Electronics and IT Industries, Proceeding, MATEIT-2008, September 26-28, 2008, @ Delhi

3. J. Lele, U. More, A. Jadhav, "Parameterization and Modeling of Voice Signal for Speaker or Speech Recognition," International Conference, INTED 2009, February 2009, Spain. (Accepted for Virtual Presentation)

4. U.More, J.Lele and A.Jadhav, "Speaker and Speech Recognition Using MFCC", Proceedings of the International Conference, ICVCom 2009, 14– 18 April 2009, Saintgits College of Engineering, Mahatma Gandhi University, Kottayam, Kerala. Paper ID 1307,

5. Ajay D.Jadhav, Anil R. Yardi, "Folding ADC Design for Software Defined GSM Radio-Mobile Station Receiver," International Journal of Computer Science and Network Security (IJCSNS), Vol. 8, No. 9, September 2008.(ISSN: 1738-7906)

6. U. More, J. Lele, A. Jadhav, "Speaker and Speech Recognition Using MFCC," International Journal of Information Technology and Engineering (IJITAE), Vol. 3, January-June 2009. (ISSN: 0973-743X)

2009-2010

1. S.Y.Tamboli, A.D.Jadhav, "Speech Signal Transmission on Ethernet: an embedded approach", International Conference and Workshop on Emerging Technology (ICWET), February 2010, Thakur college of Engineering, Kandivali, Mumbai, (Proceedings Published by ACM New York, NY, USA, Year of publication: 2010, ISBN: 978-1-60558-812-4)

2. Jyoti Lele, Ajay Jadhav, "Neural Network Based Speaker Recognition System", Journal of Management & Information Technology (JMIT) Vol. 1, 2009, pp 63-69

3. Shashikala Channalli, Ajay Jadhav, "Steganography an Art of Hiding Data," International Journal of Computer Science and Engineering (IJCSE), Vol.1, No. 3, 2009. (ISSN: 0975-3397)

4. Shashikala Channalli, Ajay Jadhav, "Steganography-Hiding Data on Billboard Display," National Conference on Recent trends in Network Security and Cryptography (Proceedings Published by PES Bangalore), Year of publication: October 2009.

5. N.N. Ganvir, A. D. Jadhav, "Explore the Performance of the ARM Processor Using JPEG" International Journal of Computer Science and Engineering (IJCSE), Vol.2, No. 1, 2010. (ISSN: 0975-3397)

2010-2011

1. A. D. Jadhav, Samadhan Mali "RTOS Based MODBUS Protocol Implementation on ARM-7 Prpcessor", International Conference and Workshop on Emerging Technology (ICWET), February 2011, Thakur college of Engineering, Kandivali, Mumbai, Vol-II.

2. Ajay Jadhav, Samadhan Mali "Realizing MODBUS Protocol Using RTOS" International Journal of Computer Applications (IJCA) (ISBN: 0978-93-80747-75-2), Proceedings on International Conference & Workshop on Emerging Trends in Technology (ICWET) (12): 1-5; 2011

3. A. D. Jadhav, S D Mali, "RTOS Based MODBUS Protocol implementation on ARM-7 Processor", ACM Digital Library, 2011 Article, Proceeding ICWET'11, Proceeding of International Conference & Workshop of Emerging Trends in Technology Published by ACM New York, NY, USA@2011, Table of Contents ISBN:978-1-4503-0449-8.

4. A. D. Jadhav, Pooja Narendra Dhok,,"Implementation of Mean Filter on FPGA for Suppression of Image Noise", in Conf. Proc. University of Pune, MAEER's MIT, Pune, 2011.

5. A. D. Jadhav, Kakade Mandar, Nitin, "MFCC: An Approach towards Speech Recognition", in Conf. Proc. University of Pune, MAEER's MIT, Pune, 2011.

2011-2012

1 Tanvi D. Desai, Dr. A. D. Jadhav "A Review of Texture Classification", International Conference and Workshop on Recent Trends in Technology (TCET), ICWET February 2012, Thakur college of Engineering, Kandivali, Mumbai, Conference Proceedings pp.830-833.

2 Swapna D. Pahade, Ajay D. Jadhav, Poorva Waingankar "Low bit Rate Video Coding Implementation using Wavelet Transform", International Conference and Workshop on Recent Trends in Technology (TCET), ICWET February 2012, Thakur college of Engineering, Kandivali, Mumbai, Conference Proceedings pp.338-344.

3. K.V.Diwan, Dr. A. D. Jadhav "Implementation of Digital Modulator using PLD", National Conference on Emerging Trends in Electronics and Telecommunication (ETETC), December 2011, MITCOE, Pune.

4. Mukund Kulkarni, Dr. A.D. Jadhav, S. M. Devkar, Dr. D. M Yadav, "Performance Evaluation of Wavelet Based Techniques for Texture Classification", International Conference on Recent Trends In Engineering & Technology, ICRTET, March 2012 @ SNJB.s Late Sau. KB Jain college of Engineering, Chandwad [Same Paper will be published in International Journal of Electronic Communication & Soft Comp Science & Engineering (IJE CSCSE-2012).

5. Desai Tanvi Dattatraya, A. D. Jadhav, "Embedded Approach For Image Texture Classification", in Conf. Proc. University of Pune, CCOE, Pune, Apr. 2012.

6. Poulomi Goswami, A. D. Jadhav, "Evaluating The Performance Of Routing Protocols In Wireless Sensor Networks", in conf. Proc. University of Pune, CCOE, Pune, Apr. 2012.

7. Bhosale Sharvari Balasaheb, M. M. Jadhav, A. D. Jadhav, "Telemetry Monitoring System On ARM 7",in conf. Proc. University of Pune, CCOE, Pune, Apr. 2012.

8. Karanjawane Ashwini Dattatraya, Ajay D. Jadhav, "Implementation Of WSN Network Layer Protocol In HDL", in conf. Proc. University of Pune, CCOE, Pune, Apr. 23-24, 2012.

9. A. D. Jadhav, S. S. Bhosle, "PLD Based MODEM Design", in conf. Proc. University of Pune, CCOE, Pune, Apr. 2012.

10. A. D. Jadhav, S. D. Pahade, "Low Bit Rate Video Coding Implementation Using Wavelet Transform", in conf. Proc. University of Pune, CCOE, Pune, Apr. 2012.

11. A. D. Jadhav, N. N. Parandkar , "Embedded System Design for "e- Stacapult", in conf. Proc. University of Pune, CCOE, Pune, Apr. 2012.

12. A. D. Ghorpade, A. D. Jadhav, "Fuzzy Logic Based Speed Control System Comparitive study", in Int. J. of Engg. Science and Technology, Vol.4, No.05, May. 2012.

2012-2013

1. Mukund Kulkarni, Dr. A.D. Jadhav, S. M. Devkar, "Evaluation of Different Wavelet Based Techniques for Textural Classification", CiiT International journal, Coimbtur, TamilNadu

2. Sushma Bhosale, A. D. Jadhav S. A. Shirsat, "PLD Based Modem Design," in IInd Int. Conf. on Advances in Signal Processing and Communication SPC-2013,"Springer Publications, May 2013.

3. Swapna. D. Pahade, Ajay D Jadhav and Poorva Waingankar. "Low Bit Rate Video Coding Implementationon Low Cost Low Performance DSP processor" in int. J. IJCA on Emerging Trends in Technology ICWET (4), pp. 25-33, Apr. 2013.

4. S. G. Chavan, A. D. Jadhav, "Performance Analysis of Zigbee Based Mesh Network", in N. Conf. Proc. University of Pune, Apr. 2013.

5. Ajay Jadhav , "Low Bit Rate Video Coding Implementation on Low Cost Low Performance DSP Processor" , in the Int. Conf. and workshop on E&TC Engg.(ICWET-2013), Feb. 2013.

6. Sushma Bhosale, S. A. Shirsat, A. D. Jadhav, "Hardware Simulation of BPSK MODEM", in Int. Conf. and Workshop on Electronics and Telecommunication Engineering (ICWET - 2013), pp.22–23, Thakur College of Engineering and Technology, Feb. 2013.

7. S. D. Pahade, A. D. Jadhav, P. Waingankar "Low Bit Rate Video Coding Implementation using Low Cost Low Performance DSP Processor", in Int. Conf. & Workshop on Emerging Trends in Technology 2013, TCET, Mumbai.

2013-2014

1. S. S. Tikar1, A. D. Jadhav, Paul Lalthumsanga, Prasanta Sarka, "CAN to Wi-Fi Interface for Vehicles", International Journal of Advanced Research in Electronics and Communication Engineering (IJARECE) Volume 2, Issue 9, September 2013, PP 769-773.

2. Shweta Bhambure , Dr. A. D. Jadhav, Prof. S. A. Shirsat, " Simulation of IEEE 802.16e Physical Layer", IOSR Journal of Electronics and Communication Engineering (IOSR-JECE) e-ISSN: 2278-2834,p- ISSN: 2278-8735. Volume 7, Issue 4 (Sep. - Oct. 2013), PP 23-28.

2018-19

1. Dr. A.D. Jadhav, Mr. O.C. Nilakhe, Ms. P.M. Pondkule, Mr. P.N. Shendage, "Botnet detection in Low Latency Anonymous Communication, IEEE, 2018.

2020-21

1. Umashankar R. More, Sanjay N. Talbar, Ajay D. Jadhav, "Architectural Issues While Incorporating Variable Resolution Architectures of Pipeline, Dual Slope and Successive Approximation Analog to Digital Converters in Time Multiplexed Mode", International Journal of Intelligent Engineering & Systems, October 18, 2020.

2022-23

1. Umashankar R. More, Sanjay N. Talbar & Ajay D. Jadhav, "Variable Resolution Time Multiplexed Digital Architecture of ADVs for System on Chip Applications", Springer, May 2022.

2. Nilofar S. Hunnargi, Dr. Ajay D. Jadhav, A Survey on Intrusion Detection Techniques in IOT Networks, International Journal of Advance Research in Science and Engineering, Volume No.11 Issue No. 06 June 2022.

3. Sana Amin, A.D. Jadhav, "An overview of physical layer security enhancement techniques for full-duplex relay network, AIP Conference Proceedings, 2023.

4. Kiran N. Patil, Ajay D. Jadhav, "Review on Power Conversion Efficiency Improvement Strategies in RF Energy Harvesting", IEEE, August 2022

2024-25

1. Dr. Ajay D. Jadhav, "Securing IOT Networks with Dynamic Learning and LSTM- Based Attack Detection" International Conference on Recent Advances in Science, Engineering, Technology and Management, 2024.

2. Dr. Ajay D. Jadhav, "A Comparative Analysis of Different Image Steganography Algorithms", National Technical paper Contest-2025.

Personal Details:
- Birth Date: February 06, 1966
- Gender: Male
- Marital Status: Married

Place:

Date: **Dr. Ajay Dadasaheb Jadhav**