


Faculty Profile

1	Name of the Faculty:	Umashankar Ramchandra More		
	Designation	Associate Professor		
	Department	Electronics & Telecommunication		
	Email Address	Umashankar.more@dnyanshree.edu.in		
	Date of Joining the Institution	7 January 2021		
2	Academic Qualification: Ph.D.	Specialization:- Embedded VLSI Design		
	Highest Qualification:	Ph.D. (E&Tc)	Year of Highest Qualification:	2021
3	Experience in Years: 23			
	Teaching	21	Professional	2
4	Area(s) of Research	System on Chip (Analog to Digital Converters)		
5	Number of Publications in reputed journals: 13			
	National Journals Papers	00		
	International Journals Papers	13		
6	Number of Conference Papers Published			27
7	Number of Books/Book chapter's Published			01
8	Number of Patents published/awarded			02
9	Consultancy Work and Social Contributions: 01-			
	Consultancy work to diagnose the fault of servo motor drive at Nichrome Industries, Shindewadi MIDC, Shirwal, 20 January, 2016. Participate in Blood Donation Camp: In AY 2022-23 @ Dnyanshree Institute of Engineering & Technology, Satara.			
10	Department Responsibilities:			
	Faculty			
11	Institute Responsibilities:			
	Vice Principal (Academics), IQAC Coordinator			
12	University Responsibilities:			
	Paper setter, Assessment, Examination Conduction			
13	Awards & Honors:			
	<ol style="list-style-type: none"> 1. Resource Person, Outcome Based Education, DIET, Satara, 19 June, 2020. 2. Guest off-campus faculty: Birla Institute of Technology & Science, (BITS Pilani), Work Integrated Learning Programmes Division, M. Tech. Embedded Systems at Cluster Programme, Pune), Second Semester 2019-20, Subject: Reconfigurable computing. 3. Coordinator, 'Microchip Development Lab' set up at ECE Department, MITSOE. August 29, 2019. 4. External Committee Member – NAAC Work Assessment at DIET, Satara, AY 2018-19 5. Subject Expert-Faculty Selection Committee at DIET, Satara in AY 2017-18 6. Resource person on 'VLSI Tools and Techniques', DIET, Satara, 23 March 2018. 7. Guest off-campus faculty: Birla Institute of Technology & Science, Pilani, Work Integrated Learning Programmes Division, M. Tech. Embedded Systems at Cluster Programme, Pune), Second Semester 2017-18, Subject: Reconfigurable computing. 			

8. Guest off campus faculty: Birla Institute of Technology & Science, Pilani, Work Integrated Learning Programmes Division, M. Tech. Embedded Systems at Cluster Programme, Pune), Second Semester 2016-17, Subject: VLSI Architecture.
9. Coordinator, Laboratory setup at DIET, Satara. e-Yantra Lab Set-up Initiative (eLSI), IIT Bombay, A project supported by MHRD through the national mission on education through ICT(NMEICT) to expand Embedded Systems and Robotics Education at academics in AY 2016-17.
10. Guest off campus faculty: MTech Microelectronics Course at Wipro Technologies, Hinjewadi, Conducted by BITS Pilani, Pune branch, since August 2016. Subject taught is Analog IC Design.
11. A guest lecture on “VHDL” for second year computer engineering students, on 9th Sept. 2015, at DYPCOE, Ambi, Talegaon, Pune.
12. A guest lecture on “Practicals in new VLSI Design & Tech. 2012 syllabus”, in Department of E&TC, delivered on 11-13 June, 2015 at **SCOE, Pune.**
- 13. Research Grant from BCUD, Savitribai Phule Pune University, Pune, in academic year 2014-15, for duration 2014-2016.**
- 14. Appreciation letter from Institute, on successful coordination of “Intelligent Embedded System Design with Intel Galileo”, 3 days’ certification program 16–18 February, 2015, Sinhgad College of Engineering, Pune (Maharashtra).**
15. A guest lecture on “VLSI”, in Department of E&TC, delivered on 25 January, 2014 at **Purandar College of Engineering & Management Research, Pune.**
16. An expert lecture on “*Analog CMOS VLSI Design*”, in Department of E&TC, delivered on October 05, 2013 at **PICT, Pune.**
17. An expert lecture series on “*Digital CMOS Design*”, for M.E. E&TC (Embedded & VLSI Design) students, in Department of E&TC, delivered between Aug. 2013 to October, 2013 at NBN College of engineering, Pune.
- 18. “Digital Electronics syllabus exploration” S.E. E&TC workshop, resource person, University of Pune, Pune.**
19. An expert lecture series on “*Wireless System Design*”, in Department of E&TC, delivered between 2 March to 15 April, 2013 at **D.Y. Patil College of Engineering & Technology, Pimpri, Pune.**
20. An expert lecture on “*Analog CMOS VLSI Design*”, in Department of E&TC, delivered on October 10, 2012 at **PICT, Pune.**
21. An expert lecture series on “*Wireless System Design*”, in Department of E&TC, delivered between April 8 and 15, 2012 at **D.Y. Patil College of Engineering & Technology, Pimpri, Pune.**
22. An expert lecture on “*VLSI Design*”, in Department of E&TC, delivered on September 25, 2011 at **Nutan Maharashtra Institute of Engineering & Technology, Pune.**
23. A guest lecture on “*VLSI Tools and Technology*”, in MSBTE sponsored Faculty Development Programme conducted on September 3-7, 2007 named as “Micro Controller Interfacing”, organized by **Bhivarabai Sawant Polytechnic, Wagholi, Pune.**
- 24. Appreciation letter from Institute, on successful coordination of “Embedded System Design”, 15 days National level short term training program, 10–22 May, 2010, Sinhgad College of Engineering, Pune (Maharashtra)**

Personal Details:

14	Birth Date	18/01/1975		
	Gender	Male	Marital Status	Married
	Permanent Address	B 301, Sai Mystique, Ambegaon (Bk), Pune 46		
	Correspondence Address	B 301, Sai Mystique, Ambegaon (Bk), Pune 46		



Signature of Faculty: